

Application No. 10/621,005
Inventor(s): Janardhanan S. Ajit
Title: SUB-MICRON HIGH INPUT VOLTAGE
TOLERANT INPUT OUTPUT (I/O) CIRCUIT
WHICH ACCOMMODATES LARGE POWER
SUPPLY VARIATIONS
Supp. Amdt. Dated December 2, 2004
Annotated Sheet 6 of 23

FIG.6

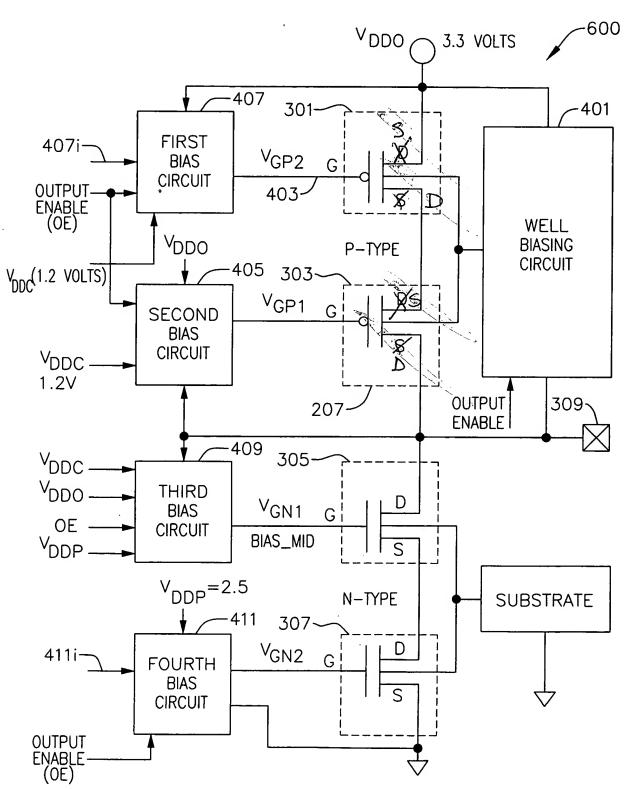




FIG.9A

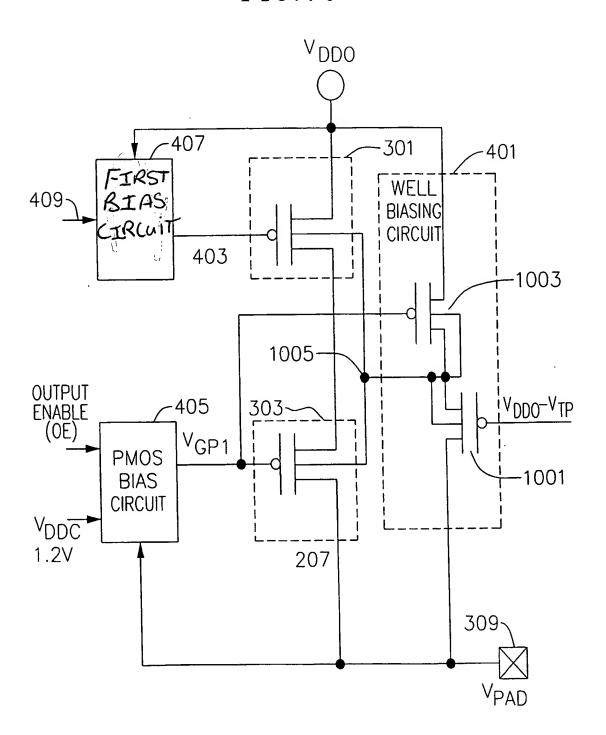
Application No. 10/621,005 Inventor(s): Janardhanan S. Ajit Title: SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT (I/O) CIRCUIT WHICH ACCOMMODATES LARGE POWER SUPPLY VARIATIONS Supp. Amdt. Dated December 2, 2004 Annotated Sheet 9 of 23

BIAS\_MID Voop Voop 606 VDDP 907 -903 905 BIAS\_ Vpbo OE.



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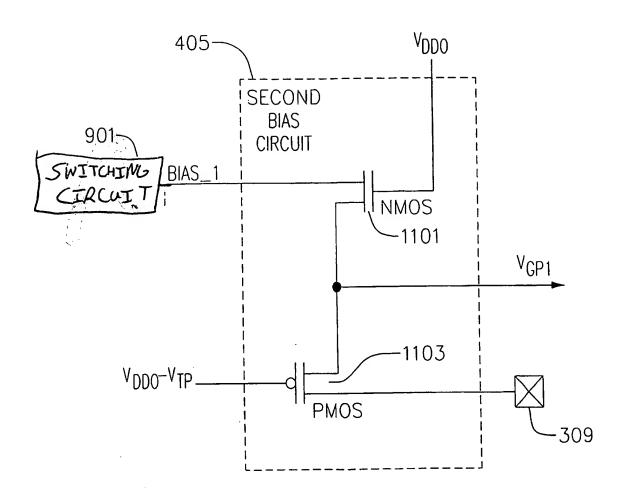
FIG. 10





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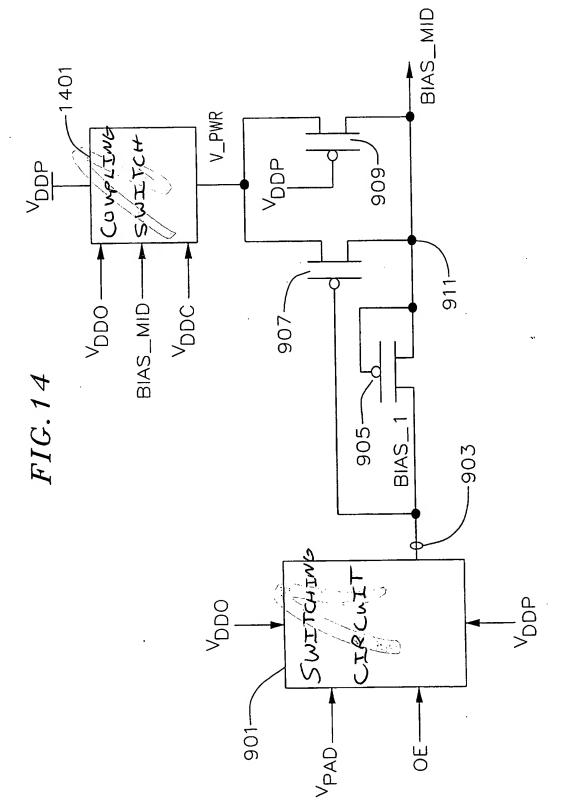
FIG. 11A



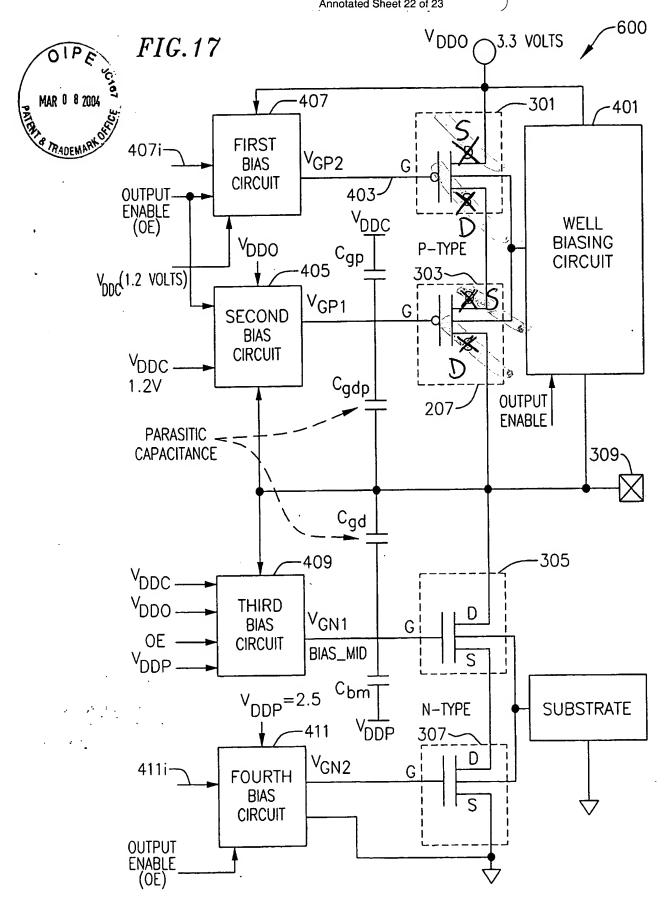
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Annotated Sheet 17 of 23 V<sub>PAD</sub> .1214 1220 1201 1203 1223 1225 1205 1209 1211  $\overline{\Lambda}^{DDO}$ 1207 1213 1219-1217 V<sub>DDO</sub> BIAS\_MID SB OV<sub>SSC</sub>OR V<sub>DDC</sub>OR V<sub>DDP</sub> -1255



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## FIG. 18

